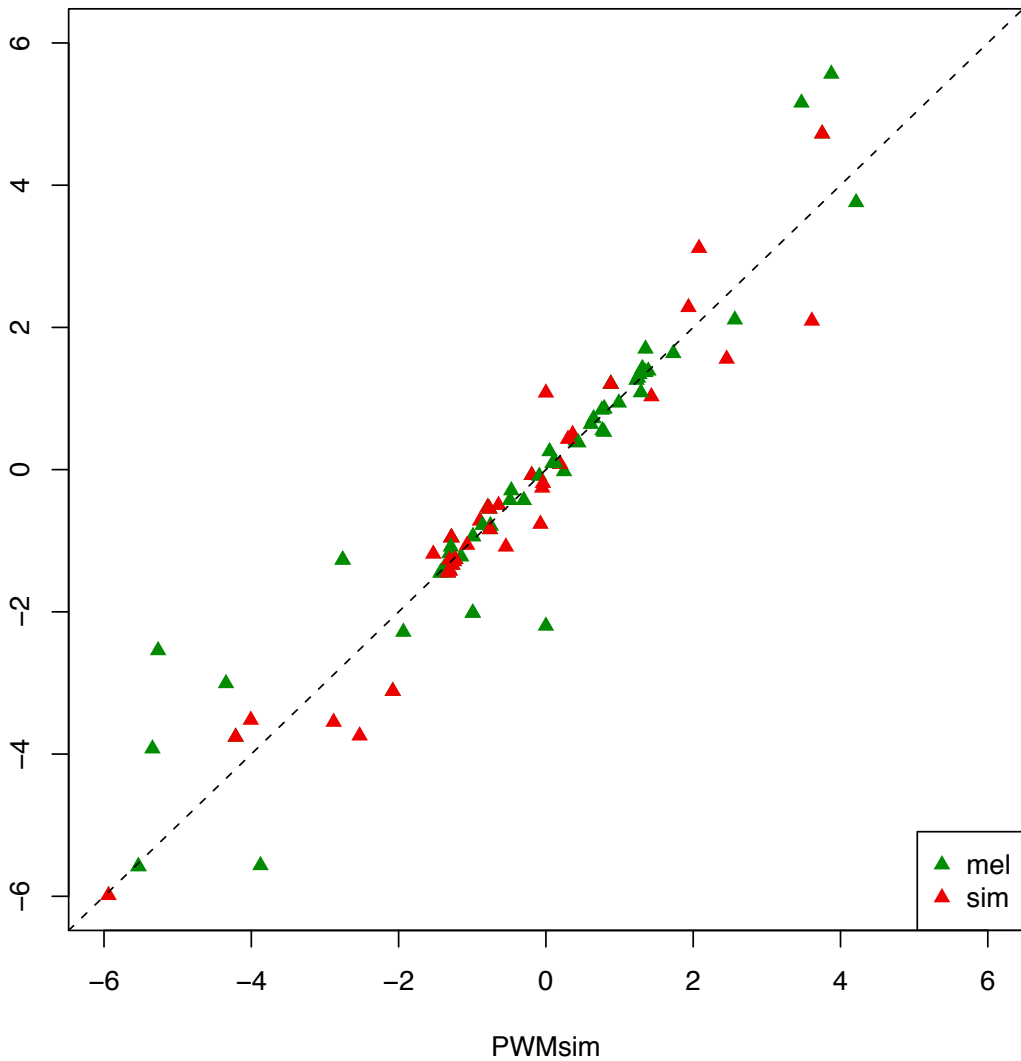


PWMMel



▲ mel  
▲ sim